



PATENT
Attorney Docket No. SEL 205 DIV

#6/B
Letter
7/30/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:)
Yukio Tanaka)
Serial No.: 10/034,797)
Filed: December 28, 2001)
Examiner: John B. Nguyen)
Art Unit: 2819)
For: D/A CONVERSION CIRCUIT AND)
SEMICONDUCTOR DEVICE)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents
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Commissioner for Patents
Alexandria, VA 22313-1450

AMENDMENT A

Sir/Madam:

In response to the Office Action mailed February 13, 2003, please amend the above-identified patent application as follows.

IN THE CLAIMS:

Please cancel claim 1.

Please add new claims 16-33 as follows.

16. (New) A D/A conversion circuit for supplying a gradation voltage corresponding to n-bit digital signal (n is a natural number that is equal to or larger than 2), comprising:

a first D/A conversion circuit comprising:

$(2^x + 1)$ gradation voltage lines;